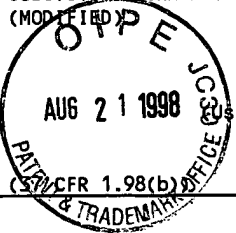


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Stratakos et al.FILING DATE
12/16/97

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U.S. PATENT DOCUMENTS

EXAMINER INITIAL		PATENT NUMBER							ISSUE DATE	PATENTEE	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
AB	AA	5	4	2	2	5	6	2	6/6/1995	Mammano et al.	323	282	

FOREIGN PATENT OR PUBLISHED FOREIGN PATENT APPLICATION

		DOCUMENT NUMBER	PUBLICATION DATE	COUNTRY OR PATENT OFFICE	CLASS	SUBCLASS	TRANSLATION	
							YES	NO

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OTHER DOCUMENTS (including Author, Title, Date, Place of Publication)

AB	AB	✓	Severns et al., <i>Modern DC-to-DC Switchmode Power Converter Circuits</i> , Van Nostrand Reinhold Co., NY, 11-77. (update)
AB	AC	✓	Lloyd Dixon, "Average Current Mode Control of Switching Power Supplies," <i>Unitrode Switching Regulated Power Supply Design Seminar Manual</i> , Unitrode Corporation, C1-1 to C1-14, 1991.
AB	AD	✓	Lloyd Dixon, "Switching Power Supply Topology Review," <i>Unitrode Switching Regulated Power Supply Design Seminar Manual</i> , Unitrode Corporation, P1-1 to P1-11, 1991.
AB	AE	✓	Kassakian et al., "DC/DC Converters," <i>Principles of Power Electronics</i> , Addison-Wesley Publishing Co., Sec. 2.3, 20-23, 1991.
AB	AF	✓	Kassakian et al., "High-Frequency Switching dc/dc Converters," <i>Principles of Power Electronics</i> , Addison-Wesley Publishing Co., Chapter 6, 103-137, 1991.
AB	AG	✓	Kassakian et al., "Dynamics and Control: An Overview," <i>Principles of Power Electronics</i> , Addison-Wesley Publishing Co., Chapter 11, 253-298, 1991.
AB	AH	✓	Kassakian et al., "Discrete-Time or Sampled-Data Models," <i>Principles of Power Electronics</i> , Addison-Wesley Publishing Co., Sec. 12.5, 313-315, 1991.
AB	AI	✓	Kassakian et al., "Feedback Control Design," <i>Principles of Power Electronics</i> , Addison-Wesley Publishing Co., Chapter 14, 365-402, 1991.

EXAMINER

A. BERHANE

DATE CONSIDERED

9-21-98

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